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FILING DATE

06/05/2006

NONCONTACT THIN FILM INTEGRATED CIRCUIT DEVICE

EXAMINER

COLEMAN, WILLIAM D

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PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

7590

31780

APPLICATION NO

10/581.674

APPLN. TYPE

nonprovisional

Number is required.

ERIC ROBINSON PMB 955 21010 SOUTHBANK ST. POTOMAC FALLS, VA 20165

Commissioner for Patents P.O. Box 1450

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CONFIRMATION NO

6916

DATE DUE

05/11/2009

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FIRST NAMED INVENTOR

Shunoei Yamazaki

PUBLICATION FEE DUE

\$300

CLASS-SUBCLASS

438-464000

2. For printing on the patent front page, list (I) the names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

TILLE OF INVENTION: METHOD FOR MANUFACTURING THIN FILM INTEGRATED CIRCUIT DEVICE, NONCONTACT THIN FILM INTEGRATED CIRCUIT DEVICE AND METHOD FOR MANUFACTURING THE SAME, AND IDTAG AND COIN INCLUDING THE

ISSUE FEE DUE

\$1510

ART UNIT

2823

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(A) NAME OF ASSIGNEE	(B) RESIDENCE: (CITY and STATE OR COUNTRY)
Semiconductor Energy Laboratory Co., Ltd.	. Atsugi-shi, Kanagawa-ken, Japan
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